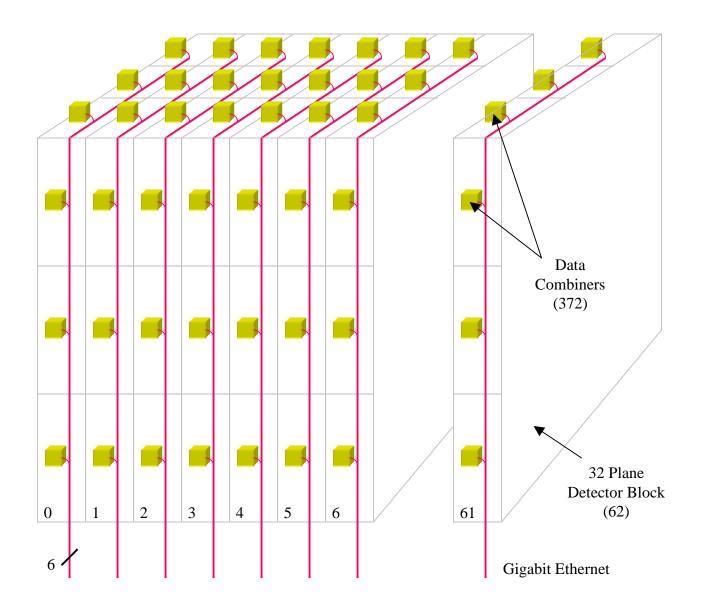
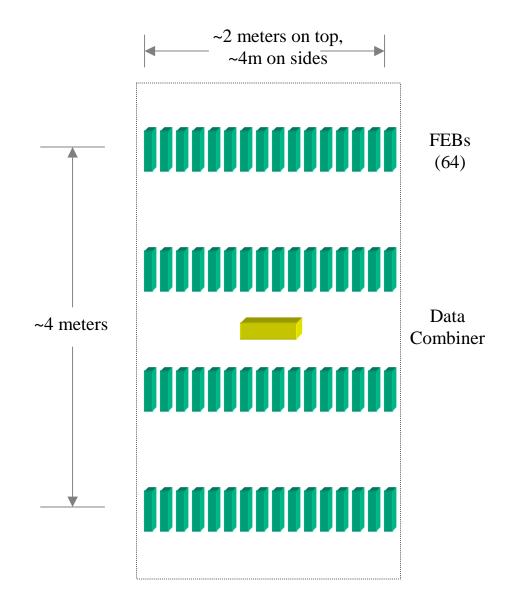
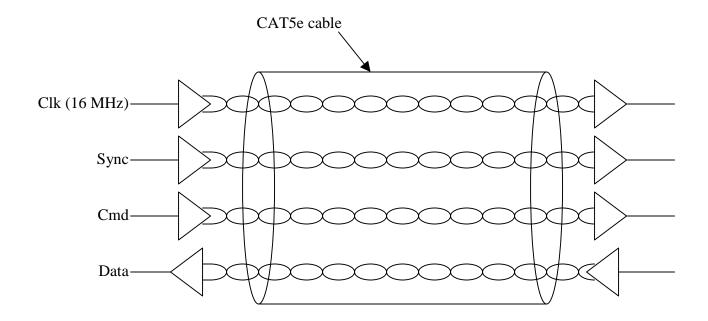
## NOvA DAQ Design

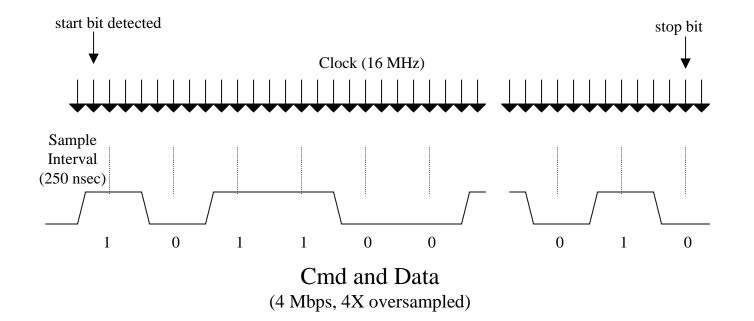
Mark Bowden's work
Presented by Leon Mualem

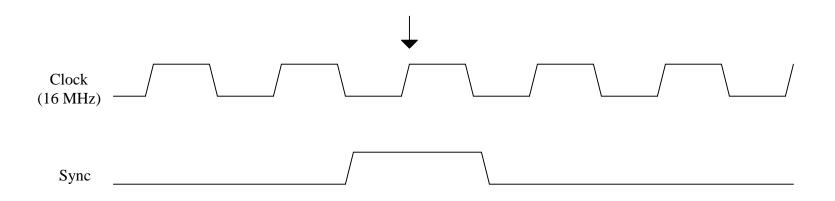




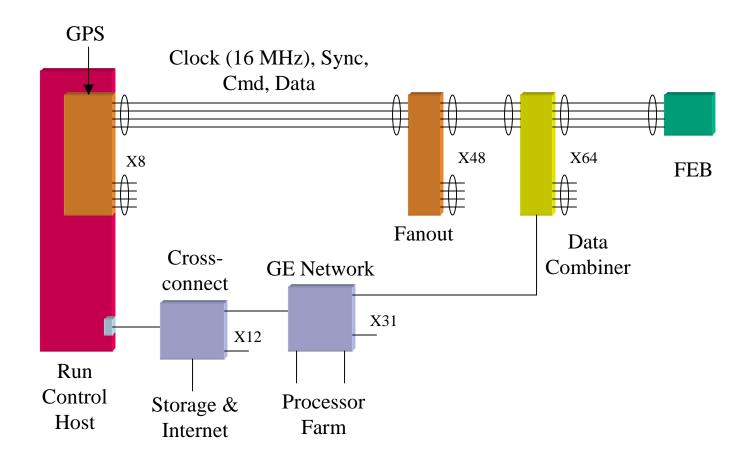


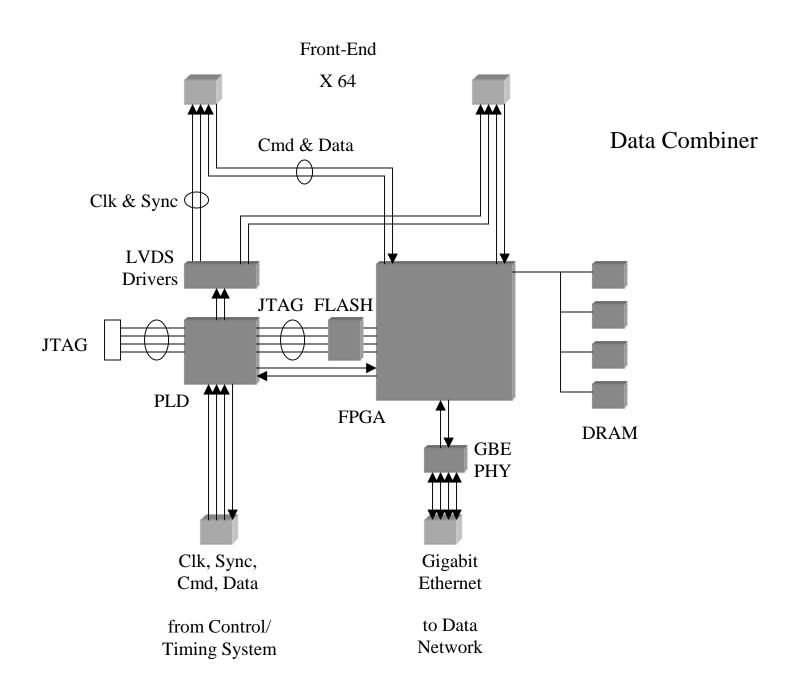
Serial Link Assignments

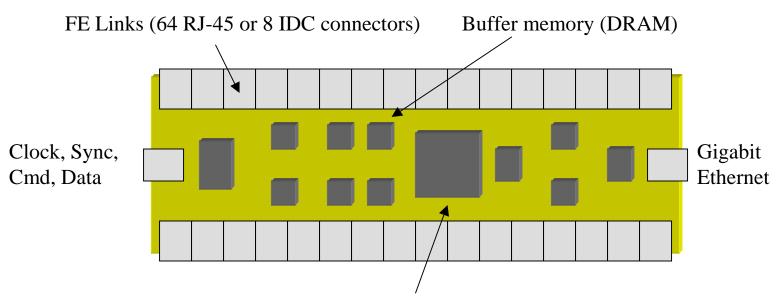




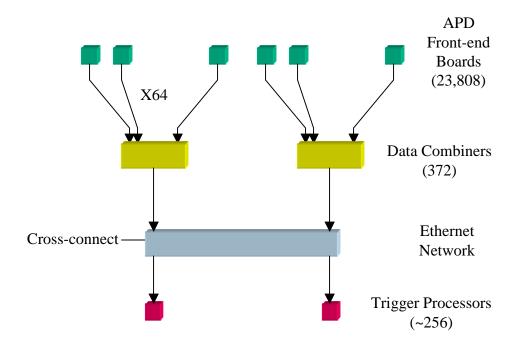
Sync



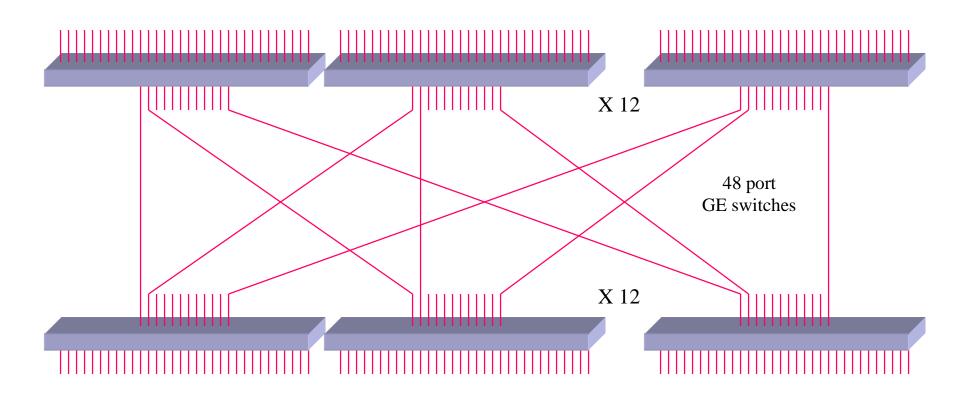




FPGA (embedded processor & Ethernet)



## **Data Combiners**



Processors

## Conclusions

- Distributed data concentrator nodes minimizes cable expense
- Star topology simplifies readout protocol
- Custom data concentrator eliminates much of underutilized ethernet switching network, allows for traffic shaping of readout system at lower cost.
  - (Value Management at work)